# C++ (& Delphi) in 12.1 Athens



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IMPORTANT: Features are not committed until completed and Generally Available (GA) released



and know lots of Delphi people watching So: cover what's new, but also what's cool in C++ that everyone can use? Goal: lots more live IDE use, than slides :)

# What's new in C++









## **Clang Upgrade in C++Builder 12.1** A new foundation for C++Builder and RAD Studio

- New version of Clang
- Revision of the entire toolchain
- Focus on platform conventions
- Focus on doing it right
- Focus on quality

o STL, linker etc: a must-use

The value of C++Builder with a high quality toolchain for modern C++







## **Technical Info of the new Clang Toolchain**

- General goal: follow Windows platform standards Leads to COFF, UCRT, PDB, etc Ο
- Uses the Itanium C++ ABI
- COFF object file format, PDB debug format Not link-compatible with a VC++ COFF object file, different C++ ABI 0
  - (goal: source compatible, handle any C++ code)
- C RTL: Uses the Windows component: Universal C Runtime (UCRT)
- C++ RTL, with enormous amount of exception handling work 700+ new tests + more
- STL: LLVM's libc++
- Linker: LLVM's IId (used for Chrome) 64-bit binaries (even in IDE) – enormous memory space







## **Clang Toolchain: VCL and FMX apps**

 VCL applications • FMX applications • DLLS, LIBS, console apps, etc • Can be 'pure C++' for these if you wish In 12.0, Delphi packages (components) are statically linked – but much faster C Mazes! linker to make up for it le the Delphi default! Never had a fast Ο enough linker to do it before These features, like dynamic Ο package linking and CMake support, coming soon

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FMX 'Mazes' app

Cell size (px): 25

Ρ



- > 🗱 Build Configurations (Release)
- V O Target Platforms (Windows 64-bit (Modern))
  - > Windows 32-bit
  - > Windows 64-bit
  - Windows 64-bit (Modern)



## **Clang Upgrade: What's Coming**

- Dynamic packages (using BPLs) coming soon Ο Ο
- CMake support also coming soon

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You can still use DLLs etc fine, this is only packages Making packages with C++ source, similarly coming soon

Parallel compilation with --jobs: coming, but use TwineCompile now (and in future too, it's good)





• See GA disclaimer

## **Clang Upgrade: Why It Matters to You!**

## Quality

- Compiler stability (ICEs) etc -> More stable!

## Compatibility

- •

## **Modernisation**

- Clang 15, much more recent. Performance
  - Linker about 4x faster

## Our foundation for C + + is future – here for you now.

Linker problems? (Eg memory issues) -> entirely new, large memory linker STL problems? (Using 3rd party C++ code or modern C++ code?) -> Much better STL!

Want to use other tools? (Eg other debuggers) -> Generates PDB! Use  $C_{+}$  + code from anywhere -> Much better compatibility!



## Feb 2024: Behind the Build for 12.1: youtube.com/watch? v=Ps5pW5uhmMw



## **Clang Upgrade: Key Differences** We've minimized differences as much as possible

- C RTL: it no longer uses the old C RTL, it uses the standard Windows C RTL • Functions coming from Turbo C don't exist any more
  - All of them have standard C replacements
  - We were worried before release but have seen very, very little code using them in the wild. And they're easy to fix. Eg, random() is replaced by rand() % x.
  - o Occasionally MS-isms, eg swapped parameter order of some obscure functions. Caught by the compiler. Easy to fix.
- Memory manager: uses UCRT MM, so can't replace with FastMM We like FastMM, but this is a result of using the standard Windows C runtime
- STL
  - This is a good difference! So much works Parallel algorithms are not available in this version





## **Clang Upgrade: Key Differences**

## Some differences that are really good

- New linker. Enough said.
- Compiler, linker etc are all 64-bit EXEs • Huge memory space
  - This includes when compiling in the IDE!
- New STL. It works very well.
- it's only got better since then.

C++ RTL: massive work on compatibility and exception handling

• In Aug 2023 (last year!) we had more C++ and EH tests passing than our then-shipping toolchain, and



## **Demo: Clang Upgrade**



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## Examples?

- A great deal of bcc64x is that it works.
- That's hard to demo.

What about the other benefits? What can Delphi folk get out of it?

Let's do some code optimisation! I'll show you something cool, but technically unsupported :)

# What can C++ folk get out of it (beyond, already 100x better)?



## **Example Code Optimisation with Clang**

- The Clang compiler is very good at optimisation.
- Data is generally FP or integer! (At its core. Strings are a set of integers.)

- This code processes a large array of floating point data. Mimics: Image processing. Scientific data processing. Engineering calculations? **Payroll processing (if you have a lot.)**
- Not intended to be real-world
- Is intended to demonstrate possibilities just by recompiling No major code modification
- Measuring just by how many gigabytes of data we can process per second o Not a 'real' measurement, but indicative





# **Example Code Optimisation with Clang**

- Take some Delphi or C++ code (I prepared both)
- Rewrite it in C++, or recompile it in C++, using the new Clang. What happens?
- Then do some more advanced (but not very advanced) things. am not an expert at what I am showing you and there will be much more you can do.
- **Delphi -> C++, multiple ways:**  Delphi abstract class / interface, implemented in C++ o https://github.com/Embarcadero/CodeRage2016/tree/master/David Millington -<u>Mixing Delphi and  $C_{++}$  plus Youtube plus blog</u> • Here, simple, just calling a DLL

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## **Demo: Optimising Math with bcc64x**



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RAD

## **Checking your CPU's instruction sets CoreInfo from SysInternals / Microsoft** https://learn.microsoft.com/en-gb/sysinternals/downloads/coreinfo

Command Prompt		×		+	~	
FPU	*		Im	ple	men <sup>.</sup>	t
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MMXEXT			Im	ple	men <sup>.</sup>	t
3DNOW			Su	ppo:	rts	
<b>3DNOWEXT</b>	_		Su	ppo:	rts	
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SSE2	*		Su	ppo:	rts	5
SSE3	*		Su	ppo	rts	\$
SSSE3	*		Su	ppo	rts	5
SSE4a	-		Su	ppo:	rts	5
SSE4.1	*		<b>NII</b>	npo:		
SSE4.2	*		Su	ppo:	rts	\$
AES	*		Su	ppo:	rts	ł
AVX	-		Su	ppo:	rts	
AVX2	-		Ju	ppo	rts	4
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AVX-512-DQ	-		Su	ppo:	rts	ļ
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i387 floating point instructions MMX instruction set AMD MMX extensions 3DNow! instructions 3DNow! extension instructions Streaming SIMD Extensions Streaming SIMD Extensions 2 Streaming SIMD Extensions 3 Supplemental SIMD Extensions 3 Streaming SINDR Extensions 4a streaming SIMD Extensions 4.1 Streaming SIMD Extensions 4.2

## AES extensions

/X instruction extensions

## AVX-512 prefetch instructions

AVX-512 exponential and reciprocal instructions



## Yes to SSE 4.1/4.2

## No to AVX / AVX2 etc 😥

## AVX2 instruction extensions 'm actually running ARN and have AVX-512 Foundation instruct AVX-512 double and quadword AVX-512 integer Fused multi NONE of these, it's emulated 🤌

## **Demo: Optimising Math with bcc64x**

- Original code
- Told it the pointers weren't aliased with \_\_\_\_restrict, it could do more optimisation
- Got some nice performance
- We built versions for SSE4 (slow, integer instructions, not useful here)
- We built with AVX2 and it failed completely We realised we can't optimise too far, because some machines don't have some instruction sets In the second second

Now we know why AVX2 doesn't work – it's not available on this CPU.



## **Optimisation via instruction sets**

 Everything was looking so good! • But suppose I want to optimise my code with AVX2 o which doesn't work on every machine • What do I do? Build three different EXEs/DLLs? SSE2 (safe target) o AVX (advanced) AVX2 and AVX-512 (modern computers) ...and ship three different files? myapp-sse2/avx/avx512.dll? Load at runtime?

We had something so promising and now it looks like a lot of work. ... or does it?

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## **Optimisation via instruction sets**

The ideal solution is: • One EXE/DLL

• with multiple different versions of the function compiled in • at runtime, without you doing anything at all, if you have an AVX2 CPU then the AVX2 version of the function is what's run

So you write your code once. When compiled, it's optimised for everything. Runtime dispatch based on your CPU. It just runs, no matter what hardware your users have.





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## The real C++ Way

This code converted Delphi to C++ Memory allocation is still Delphi Initialising the values is still Delphi • We're accessing an array by pointer like it's 1999 • What if we did this all C++ and in a modern style?



![](_page_22_Picture_2.jpeg)

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## The real C++ way – what did we see?

• Used a vector Reserved memory (to avoid multiple reallocations as add items) Delphi did this too, via SetLength, which actually created all the array elements – reserve reserves the memory but the count of elements remains 0 Loop to add them, this is 'embarrassingly parallelizable' too just like the math • In the math method, use iterators Iterating over two collections at once has no really elegant solution o I iterated over one, and incremented the iterator for the other Note the code assumed the same length, there was no safety checking

![](_page_23_Picture_4.jpeg)

## The real C++ way – results

Original Delphi code:  $4\frac{1}{2}$  seconds (Win64) Original C++ code:  $4\frac{1}{2}$  seconds (old Win64) New C++ code: 0.57 seconds (new Win64!) Eight times faster!

## Time for the entire app – allocate memory, initialise, do math, exit

![](_page_24_Figure_5.jpeg)

Time

![](_page_24_Figure_6.jpeg)

![](_page_24_Picture_7.jpeg)

![](_page_25_Picture_1.jpeg)

# Clang being awesome

![](_page_25_Picture_3.jpeg)

![](_page_25_Picture_4.jpeg)

- Started with some basic math code

- o Function version chosen at runtime

Big speed bump using new Win64 Modern Clang Further speed bumps with \_\_\_\_\_restrict for nonaliased pointers, etc  $\circ$  If allocating in C++, could allocate on aligned memory I expected more performance here, need to look at assembly See: 'Restrict Qualified Pointers in LLVM' by Hal Finkel Optimising for specific instruction sets, eg SSE2 How this is great but fails when hardware doesn't support those instruction sets Optimising for multiple instruction sets at the same time • Not supported :) We target SSE2/3 by default • Making use of modern C++ – entire app is faster

![](_page_26_Picture_14.jpeg)

![](_page_26_Picture_22.jpeg)

Floating point math, GB/sec by Toolchain - taller is better!

![](_page_27_Figure_3.jpeg)

## \* Measured by running 3 times -> mean. Running on emulated ARM hardware

Toolchain

![](_page_27_Picture_8.jpeg)

- Traditional Win64 a little better than Win32
- Jump to the Win64 Modern Clang immense!
- Sanity check: bcc64x similar in C++ app, and Delphi app
- restrict gives small boost
- (Not shown: C++ with 16-byte alignment, similar)
- Should be able to get (much?) more – I am not an optimisation expert, this seems a good demo of possibilities
- Can build for all instructions sets at once and get best possible per hardware 😍

Modern, Multiple instruction sets

![](_page_27_Figure_17.jpeg)

![](_page_27_Figure_18.jpeg)

![](_page_27_Figure_19.jpeg)

![](_page_27_Figure_20.jpeg)

![](_page_27_Figure_21.jpeg)

![](_page_27_Figure_22.jpeg)

Floating point math, GB/sec by Toolchain - taller is better!

![](_page_28_Figure_3.jpeg)

## \* Measured by running 3 times -> mean. Running on emulated ARM hardware

Toolchain

![](_page_28_Picture_8.jpeg)

## Who benefits?

## Anyone where performance is important

- This was floating point. Try integer operations – like string manipulation! Lots of SSE4 and AVX instructions there
  - Funny story while writing the presentation...
- Optimisation is not as simple as turning on a switch, have to write code so the compiler can optimise it

We did not analyse the assembly at all

Modern, Multiple instruction sets

## **Optimisation thoughts**

- (Did I mention, not an expert on this area?)
- Look for vectorisation possibilities. 'Embarrassingly parallel'. Keep in mind data alignment, avoid data overlap, tell compiler what's safe
- Eg: records of name, DOB, address, age. Each is a row o Thousands of (slow) virtual method calls iterating over floating point -> a single virtual method call that iterated over a large table of FP
- You want to calculate, say, average age • Change to a column, so have a set of numbers: process all of them in one go • The best speedup I ever got in my career was doing this: 260x.

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Look at other libraries like libsimdpp for low-level primitives

![](_page_29_Picture_12.jpeg)

## Takeaways

- New C++ Win64 Modern
  - o Built to 'do it right'
  - o Immensely better quality

  - Solves C++ problems re STL, linker, etc
  - o Use it today, more coming (eg dynamic packages) soon!
- Compiled code performance
  - o Immense jump, old to new o 4x faster than Delphi & C++ Win32 o 3x faster than Delphi & C++ Win64 (old)
  - o 8x faster when it's all C++
  - **Unsupported**, but try multi-targets :)

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# Matches platform standards (eg PDB, COFF)

- 40

![](_page_30_Picture_18.jpeg)

Floating point math, GB/sec by Toolchain - taller is better!

![](_page_30_Figure_20.jpeg)

Toolchair

# Live C S A david.millington@embarcadero.com

![](_page_31_Picture_1.jpeg)

• 'Restrict Qualified Pointers in LLVM' by Hal Finkel: https://llvm.org/devmtg/2017-02-04/Restrict-Qualified-Pointers-in-L LVM.pdf • CPU dispatch / targets:

## New C++ Win64 Modern does it right New linker, STL, huge quality, etc

Floating point math, GB/sec by Toolchain - taller is better!

![](_page_31_Figure_8.jpeg)

clickhouse.com/blog/cpu-dispatch-in-clickhouse o <a href="https://github.com/Embarcadero/CodeRage2016/tree/master/David">https://github.com/Embarcadero/CodeRage2016/tree/master/David</a> <u>Millington - Mixing Delphi and C++ (remember packages coming)</u> o https://stackoverflow.com/questions/64580921/c-aligned-new

![](_page_31_Figure_10.jpeg)

![](_page_32_Picture_0.jpeg)